	Application No.	Applicant(s)
Notice of Allowability	10/074,591 Examiner	LIM ET AL.  Art Unit
•		
	Un C Cho	2687
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT Re of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communicatior GHTS. This application is subject t	plication. If not included  n will be mailed in due course. THIS
1. This communication is responsive to <u>25 January 2005</u> .		
2. The allowed claim(s) is/are <u>1,8-11 and 17-25</u> .		
3. The drawings filed on <u>13 February 2002</u> are accepted by the Examiner.		
<ul> <li>4.</li></ul>		
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/0-Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Da 8), 7. ☐ Examiner's Amendo	te

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## **DETAILED ACTION**

## Allowable Subject Matter

- 1. Claims 1, 8 11 and 17 25 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

Regarding claim 1, Yamaguchi in view of Damgaard discloses a radiofrequency apparatus capable of transmitting radio-frequency signals, the radiofrequency apparatus comprising a voltage-controlled oscillator circuitry (voltagecontrolled oscillator, Fig. 1, 32) configured to generate a first signal that has a first frequency (output of the voltage-controlled oscillator) and a divider circuitry (frequency division and distribution section, Fig. 1, 33) being responsive to the first signal, the divider circuitry configured to generate a second signal (output of the frequency division and distribution section) that has a second frequency, wherein the second frequency equals the first frequency divided by a number (Yamaguchi, Col. 3, lines 13 – 23); a second switch coupled to the divider circuitry (Switch, Fig. 2, 55, coupled to the frequency divider), the second switch configured to generate a switched second signal by selectively supplying the second signal to a second output (Yamaguchi, Col. 6, lines 4 – 16); a feedback circuitry coupled to the voltage-controlled oscillator circuit (signal, Fig. 1, 67, coming out of VCO, Fig. 1, 57 or 59 and feeding back), the feedback circuitry configured to adjust the first frequency (Damgaard, Col. 4, lines 43 – 60); the feedback circuitry (feedback circuitry composed of PLL) adjusts the first frequency in response to a feedback signal (signal, Fig. 1, 67) derived from the

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switched (combiner or switch, Fig. 1, 63) first and second signals (signals coming out of the VCO, Fig. 1, 57 or 59) (Damgaard, Col. 4, lines 43 – 60).

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However, Yamaguchi and Damgaard either alone or in combination fails to teach that the feedback circuitry comprises a third switch coupled to the first and second switches, the third switch configured to selectively supply as the feedback signal one of the switched first and second signals.

Regarding claim 11, the claim is interpreted and allowed for the same reason as set forth in claim 1.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Un C Cho whose telephone number is (571) 272-7919. The examiner can normally be reached on M ~ F 8:00AM to 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lester Kincaid can be reached on (571) 272-7922. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Un C Cho 6/2/05 UC Examiner

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